

## IEEE DONALD O. PEDERSON AWARD IN SOLID-STATE CIRCUITS

### RECIPIENTS

*(Formerly known as the IEEE SOLID-STATE CIRCUITS AWARD)*

- 2017 - TAKAO NISHITANI  
Representative Partner, Laisip,  
Kanagawa, Japan
- AND
- JOHN S. THOMPSON  
Consultant and President (Retired),  
Technology Pathways, Tucson,  
Arizona, USA
- 2016 – MILES A. COPELAND  
Distinguished Research Professor  
(Retired), Carleton University,  
Ottawa, Ontario, Canada
- 2015 - ROBERT WHITLOCK ADAMS  
Fellow, Analog Devices, Inc., Acton,  
Massachusetts, USA
- 2014 - ROBERT G. MEYER  
Professor Emeritus and Professor in  
the Graduate School, Department of  
Electrical Engineering and Computer  
Sciences, University of California,  
Berkeley, CA USA
- 2013 - ANANTHA P. CHANDRAKASAN  
Professor, Electrical Engineering and  
Computer Science, Massachusetts  
Institute of Technology, Cambridge,  
MA, USA
- 2012 - BEHZAD RAZAVI  
Professor, University of California,  
Los Angeles, CA, USA
- 2011 - WILLY SANSEN  
Emeritus Professor, Katholieke  
Universiteit Leuven,  
Leuven, Belgium
- 2010 – TAKAYASU SAKURAI  
Professor, Inst of Industrial Science  
Univ of Tokyo  
Tokyo, Japan
- 2009 – TERESA H. MENG  
Professor of Electrical Engineering,
- “For pioneering real-time programmable digital signal processor architectures.”
- “For contributions to the design and application of switched-capacitor and RF signal processing circuits.”
- “For contributions to noise-shaping data converter circuits, digital signal processing, and log-domain analog filters.”
- “For pioneering contributions to the design and modeling of analog and radio-frequency circuits.”
- “For pioneering techniques in low-power digital and analog CMOS design.”
- “For pioneering contributions to the design of high-speed CMOS communication circuits.”
- “For leadership in analog integrated circuit design.”
- “For pioneering contributions to the design and modeling of high-speed and low-power CMOS logic circuits.”
- “For pioneering contributions to the development of integrated wireless

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- |   |   |
|---|---|
| Stanford University,<br>Stanford, CA, USA   | communications systems."  |
| 2008 - ASAD A. ABIDI<br>Professor, Electrical Engineering,<br>University of California,<br>Los Angeles, CA, USA | "For pioneering and sustained contributions<br>in the development of RF-CMOS"   |
| 2007 – HUGO DE MAN<br>Dept of EE, Katholieke Universiteit<br>Leuven,<br>Leuven, Belgium                         | "For leadership in integrated circuit design<br>and design methodology."  |
| 2006 - MARK A. HOROWITZ<br>Professor of EE and CS,<br>Stanford University,<br>Stanford, CA, USA                 | "For pioneering contributions to the design<br>of high-performance digital integrated<br>circuits and systems."                 |
| 2005 – BRUCE A. WOOLEY<br>Professor<br>Stanford University,<br>Stanford, CA, USA                                | "For pioneering contributions to integrated<br>electronics for analog-to-digital data<br>conversion in communications systems." |
| 2004 – ERIC A. VITTOZ<br>Professor and Chief Scientist<br>CSEMS.A.,<br>Neuchatel, Switzerland                   | "For pioneering contributions to low-power<br>device modeling and CMOS circuit design."   |
| 2003 - DANIEL DOBBERPUHL<br>Vice President and General Manager<br>Broadcom Corporation,<br>San Jose, CA, USA    | "For pioneering design of high-speed and<br>low-power microprocessors."   |
| 2002 – CHENMING HU<br>University of California,<br>Berkeley, CA, USA  | "For contributions to MOSFET physics and<br>development of the BSIM model for CMOS<br>circuit simulation."                      |
| AND<br>PING-KEUNG KO<br>Authosis, Inc.,<br>Quarry Bay, Hong Kong  |   |
| 2001 – No Recipient   |   |
| 2000 - ROBERT H. KRAMBECK<br>Tandem Computers,<br>Cupertino, CA, USA  | "For pioneering the introduction and<br>implementation of domino CMOS logic."   |
| AND   |   |

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HUNG-FAI (STEPHEN) LAW  
Alaris, Inc.,  
Fremont, CA, USA

- |   |  |
|---|--|
| 1999 - KENSALL D. WISE<br>University of Michigan<br>Ann Arbor, MI, USA                  | "For pioneering contributions to the development of solid-state sensors, circuits and integrated sensing systems." |
| 1998 - NICKY CHAU-CHUN LU<br>Etron Technology Inc.<br>Hsinchu, Taiwan                   | "For pioneering contributions to high speed dynamic memory design and cell technology."                            |
| 1997 - ROBERT W. BRODERSEN<br>University of California,<br>Berkeley, CA, USA            | "For contributions to the design of integrated circuits for signal processing systems."                            |
| 1996 - RUDY J. VAN DE PLASSCHE<br>Philips Research Labs<br>Eindhoven, The Netherlands   | "For pioneering contributions to the design of integrated circuits for data conversion."                           |
| 1995 - LEWIS M. TERMAN<br>IBM/T. J. Watson Research Center<br>Yorktown Heights, NY, USA | "For leadership in the field of MOS devices and circuits for semiconductor memories."                              |
| 1994 - PAUL R. GRAY<br>University of California<br>Berkeley, CA, USA                    | "For contributions to analog integrated circuit design, especially for MOS switched capacitor circuits."           |
| 1993 - KIYOO ITOH<br>Hitachi, Ltd.<br>Tokyo Japan                                       | "For technical contributions to folded data-line circuits and the development of high-density dynamic RAMs."       |
| 1992 - BARRIE GILBERT<br>Analog Devices<br>Beaverton, OR, USA                           | "For contributions to non-linear analog signal processing circuits."   |
| 1991 - FRANK WANLASS<br>Standard Micro Sys.<br>San Jose, CA, USA                        | "For the invention of Complementary MOS (CMOS) Logic Circuitry."   |
| 1990 - TOSHIAKI MASUHARA<br>Hitachi, Ltd.<br>Tokyo Japan                                | "For pioneering contributions to NMOS depletion-load circuits and the development of high speed CMOS memories."    |
| 1989 - JAMES D. MEINDL<br>Rensselaer Polytechnic Institute,                             | "For contributions to solid-state circuits and solid-state circuit technology."                                    |

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Troy, NY, USA