

# Workshop on Manufacturing Issues Beyond the 70 nm Node

In conjunction with 2001 International Symposium on Semiconductor Manufacturing, a half day “Workshop on Manufacturing Issues Beyond the 70 nm Node” was organized on Sunday October 7, 2001 at Fairmont Hotel, San Jose, California. The workshop was organized by the IEEE EDS technical committee of Semiconductor Manufacturing. The workshop consisted of 8 invited talks followed by the panel discussion. About 80 persons attended the workshop. A workbook consisting of the collection of the written material provided by the speakers and panel-discussion members was distributed to the workshop attendees. Rajendra Singh of Clemson University, Robert Doering of Texas Instruments, Inc. and Hidetoshi Koike of Toshiba Corporation organized the workshop.



Workshop organizers, invited speakers and panel members