

IEEE DONALD O. PEDERSON AWARD IN SOLID-STATE CIRCUITS

RECIPIENTS

(Formerly known as the IEEE SOLID-STATE CIRCUITS AWARD)

- 2021 – A. PAUL BROKAW
Retired, Analog Devices, Inc.,
Tucson, Arizona, USA
“For leadership in the design of voltage references, amplifiers, and power management, and for contributions to the principles of analog circuit design.”
- 2020 – KLAAS BULT
Professor, Delft University of
Technology – Fac. EEMCS, Delft,
Zuid Holland, the Netherlands
“For leadership in embedded analog and mixed-signal integrated circuits.”
- 2019 – LAURENCE W. NAGEL
President, Omega Enterprises
Consulting, Kensington, California,
USA
“For the development and demonstration of SPICE as a tool to design and optimize electronic circuits.”
- 2018 - WILLIAM S. CARTER
Fellow, Xilinx, San Jose, California,
USA
“For contributions to field-programmable gate array technology.”
- AND
STEPHEN M. TRIMBERGER
Fellow, Xilinx, San Jose, California,
USA
- 2017 - TAKAO NISHITANI
Representative Partner, Laisip,
Kanagawa, Japan
“For pioneering real-time programmable digital signal processor architectures.”
- AND
JOHN S. THOMPSON
Consultant and President (Retired),
Technology Pathways, Tucson,
Arizona, USA
- 2016 – MILES A. COPELAND
Distinguished Research Professor
(Retired), Carleton University,
Ottawa, Ontario, Canada
“For contributions to the design and application of switched-capacitor and RF signal processing circuits.”
- 2015 - ROBERT WHITLOCK ADAMS
Fellow, Analog Devices, Inc., Acton,
Massachusetts, USA
“For contributions to noise-shaping data converter circuits, digital signal processing, and log-domain analog filters.”
- 2014 - ROBERT G. MEYER
Professor Emeritus and Professor in
the Graduate School, Department of
Electrical Engineering and Computer
Sciences, University of California,
Berkeley, CA USA
“For pioneering contributions to the design and modeling of analog and radio-frequency circuits.”

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|--|---|
| 2013 - ANANTHA P. CHANDRAKASAN
Professor, Electrical Engineering and
Computer Science, Massachusetts
Institute of Technology, Cambridge,
MA, USA | "For pioneering techniques in low-power
digital and analog CMOS design." |
| 2012 - BEHZAD RAZAVI
Professor, University of California,
Los Angeles, CA, USA | "For pioneering contributions to the design
of high-speed CMOS communication
circuits." |
| 2011 - WILLY SANSEN
Emeritus Professor, Katholieke
Universiteit Leuven,
Leuven, Belgium | "For leadership in analog integrated circuit
design." |
| 2010 - TAKAYASU SAKURAI
Professor, Inst of Industrial Science
Univ of Tokyo
Tokyo, Japan | "For pioneering contributions to the design
and modeling of high-speed and low-power
CMOS logic circuits." |
| 2009 - TERESA H. MENG
Professor of Electrical Engineering,
Stanford University,
Stanford, CA, USA | "For pioneering contributions to the
development of integrated wireless
communications systems." |
| 2008 - ASAD A. ABIDI
Professor, Electrical Engineering,
University of California,
Los Angeles, CA, USA | "For pioneering and sustained contributions
in the development of RF-CMOS" |
| 2007 - HUGO DE MAN
Dept of EE, Katholieke Universiteit
Leuven,
Leuven, Belgium | "For leadership in integrated circuit design
and design methodology." |
| 2006 - MARK A. HOROWITZ
Professor of EE and CS,
Stanford University,
Stanford, CA, USA | "For pioneering contributions to the design
of high-performance digital integrated
circuits and systems." |
| 2005 - BRUCE A. WOOLEY
Professor
Stanford University,
Stanford, CA, USA | "For pioneering contributions to integrated
electronics for analog-to-digital data
conversion in communications systems." |
| 2004 - ERIC A. VITTOZ | "For pioneering contributions to low-power |

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- | | |
|--|--|
| Professor and Chief Scientist
CSEMS.A.,
Neuchatel, Switzerland | device modeling and CMOS circuit design." |
| 2003 - DANIEL DOBBERPUHL
Vice President and General Manager
Broadcom Corporation,
San Jose, CA, USA | "For pioneering design of high-speed and low-power microprocessors." |
| 2002 - CHENMING HU
University of California,
Berkeley, CA, USA | "For contributions to MOSFET physics and development of the BSIM model for CMOS circuit simulation." |
| AND
PING-KEUNG KO
Authosis, Inc.,
Quarry Bay, Hong Kong | |
| 2001 - No Recipient | |
| 2000 - ROBERT H. KRAMBECK
Tandem Computers,
Cupertino, CA, USA | "For pioneering the introduction and implementation of domino CMOS logic." |
| AND
HUNG-FAI (STEPHEN) LAW
Alaris, Inc.,
Fremont, CA, USA | |
| 1999 - KENSALL D. WISE
University of Michigan
Ann Arbor, MI, USA | "For pioneering contributions to the development of solid-state sensors, circuits and integrated sensing systems." |
| 1998 - NICKY CHAU-CHUN LU
Etron Technology Inc.
Hsinchu, Taiwan | "For pioneering contributions to high speed dynamic memory design and cell technology." |
| 1997 - ROBERT W. BRODERSEN
University of California,
Berkeley, CA, USA | "For contributions to the design of integrated circuits for signal processing systems." |
| 1996 - RUDY J. VAN DE PLASSCHE
Philips Research Labs
Eindhoven, The Netherlands | "For pioneering contributions to the design of integrated circuits for data conversion." |
| 1995 - LEWIS M. TERMAN
IBM/T. J. Watson Research Center
Yorktown Heights, NY, USA | "For leadership in the field of MOS devices and circuits for semiconductor memories." |

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- | | |
|--|---|
| 1994 - PAUL R. GRAY
University of California
Berkeley, CA, USA | "For contributions to analog integrated circuit design, especially for MOS switched capacitor circuits." |
| 1993 - KIYOO ITOH
Hitachi, Ltd.
Tokyo Japan | "For technical contributions to folded data-line circuits and the development of high-density dynamic RAMs." |
| 1992 - BARRIE GILBERT
Analog Devices
Beaverton, OR, USA | "For contributions to non-linear analog signal processing circuits." |
| 1991 - FRANK WANLASS
Standard Micro Sys.
San Jose, CA, USA | "For the invention of Complementary MOS (CMOS) Logic Circuitry." |
| 1990 - TOSHIAKI MASUHARA
Hitachi, Ltd.
Tokyo Japan | "For pioneering contributions to NMOS depletion-load circuits and the development of high speed CMOS memories." |
| 1989 - JAMES D. MEINDL
Rensselaer Polytechnic Institute,
Troy, NY, USA | "For contributions to solid-state circuits and solid-state circuit technology." |