

IEEE DONALD O. PEDERSON AWARD IN SOLID-STATE CIRCUITS

RECIPIENTS

(Formerly known as the IEEE SOLID-STATE CIRCUITS AWARD)

- 2019 – LAURENCE W. NAGEL
President, Omega Enterprises
Consulting, Kensington, California,
USA
- 2018 - WILLIAM S. CARTER
Fellow, Xilinx, San Jose, California,
USA
- AND
- STEPHEN M. TRIMBERGER
Fellow, Xilinx, San Jose, California,
USA
- 2017 - TAKAO NISHITANI
Representative Partner, Laisip,
Kanagawa, Japan
- AND
- JOHN S. THOMPSON
Consultant and President (Retired),
Technology Pathways, Tucson,
Arizona, USA
- 2016 – MILES A. COPELAND
Distinguished Research Professor
(Retired), Carleton University,
Ottawa, Ontario, Canada
- 2015 - ROBERT WHITLOCK ADAMS
Fellow, Analog Devices, Inc., Acton,
Massachusetts, USA
- 2014 - ROBERT G. MEYER
Professor Emeritus and Professor in
the Graduate School, Department of
Electrical Engineering and Computer
Sciences, University of California,
Berkeley, CA USA
- 2013 - ANANTHA P. CHANDRAKASAN
Professor, Electrical Engineering and
Computer Science, Massachusetts
Institute of Technology, Cambridge,
MA, USA
- 2012 - BEHZAD RAZAVI
Professor, University of California,
Los Angeles, CA, USA

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- | | |
|---|---|
| 2011 - WILLY SANSEN
Emeritus Professor, Katholieke
Universiteit Leuven,
Leuven, Belgium | "For leadership in analog integrated circuit design." |
| 2010 – TAKAYASU SAKURAI
Professor, Inst of Industrial Science
Univ of Tokyo
Tokyo, Japan | "For pioneering contributions to the design and modeling of high-speed and low-power CMOS logic circuits." |
| 2009 – TERESA H. MENG
Professor of Electrical Engineering,
Stanford University,
Stanford, CA, USA | "For pioneering contributions to the development of integrated wireless communications systems." |
| 2008 - ASAD A. ABIDI
Professor, Electrical Engineering,
University of California,
Los Angeles, CA, USA | "For pioneering and sustained contributions in the development of RF-CMOS" |
| 2007 – HUGO DE MAN
Dept of EE, Katholieke Universiteit
Leuven,
Leuven, Belgium | "For leadership in integrated circuit design and design methodology." |
| 2006 - MARK A. HOROWITZ
Professor of EE and CS,
Stanford University,
Stanford, CA, USA | "For pioneering contributions to the design of high-performance digital integrated circuits and systems." |
| 2005 – BRUCE A. WOOLEY
Professor
Stanford University,
Stanford, CA, USA | "For pioneering contributions to integrated electronics for analog-to-digital data conversion in communications systems." |
| 2004 – ERIC A. VITTOZ
Professor and Chief Scientist
CSEMS.A.,
Neuchatel, Switzerland | "For pioneering contributions to low-power device modeling and CMOS circuit design." |
| 2003 - DANIEL DOBBERPUHL
Vice President and General Manager
Broadcom Corporation,
San Jose, CA, USA | "For pioneering design of high-speed and low-power microprocessors." |
| 2002 – CHENMING HU | "For contributions to MOSFET physics and |

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- | | |
|---|--|
| University of California,
Berkeley, CA, USA | development of the BSIM model for CMOS
circuit simulation." |
| AND | |
| PING-KEUNG KO
Authosis, Inc.,
Quarry Bay, Hong Kong | |
| 2001 - No Recipient | |
| 2000 - ROBERT H. KRAMBECK
Tandem Computers,
Cupertino, CA, USA | "For pioneering the introduction and
implementation of domino CMOS logic." |
| AND | |
| HUNG-FAI (STEPHEN) LAW
Alaris, Inc.,
Fremont, CA, USA | |
| 1999 - KENSALL D. WISE
University of Michigan
Ann Arbor, MI, USA | "For pioneering contributions to the
development of solid-state sensors, circuits
and integrated sensing systems." |
| 1998 - NICKY CHAU-CHUN LU
Etron Technology Inc.
Hsinchu, Taiwan | "For pioneering contributions to high speed
dynamic memory design and cell
technology." |
| 1997 - ROBERT W. BRODERSEN
University of California,
Berkeley, CA, USA | "For contributions to the design of
integrated circuits for signal processing
systems." |
| 1996 - RUDY J. VAN DE PLASSCHE
Philips Research Labs
Eindhoven, The Netherlands | "For pioneering contributions to the design
of integrated circuits for data conversion." |
| 1995 - LEWIS M. TERMAN
IBM/T. J. Watson Research Center
Yorktown Heights, NY, USA | "For leadership in the field of MOS devices
and circuits for semiconductor memories." |
| 1994 - PAUL R. GRAY
University of California
Berkeley, CA, USA | "For contributions to analog integrated
circuit design, especially for MOS switched
capacitor circuits." |
| 1993 - KIYOO ITOH
Hitachi, Ltd.
Tokyo Japan | "For technical contributions to folded
data-line circuits and the development of
high-density dynamic RAMs." |
| 1992 - BARRIE GILBERT
Analog Devices | "For contributions to non-linear analog
signal processing circuits." |

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Beaverton, OR, USA

1991 - FRANK WANLASS
Standard Micro Sys.
San Jose, CA, USA

"For the invention of Complementary MOS (CMOS) Logic Circuitry."

1990 - TOSHIAKI MASUHARA
Hitachi, Ltd.
Tokyo Japan

"For pioneering contributions to NMOS depletion-load circuits and the development of high speed CMOS memories."

1989 - JAMES D. MEINDL
Rensselaer Polytechnic Institute,
Troy, NY, USA

"For contributions to solid-state circuits and solid-state circuit technology."